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Y1Y0 = State

X1X0 = input (X1 = a<b, X0=start)

Z1 = Multiplier x and Multiplier a enabled

Z0 = b x a register enabled

Fail

11

00,01

10,11

00

01,11

10

10

01,11

00

01

LOOP

10

WORK

01

dvs

Start00

I implemented a ROM to complete this assignment, the ROM will receive input and get the address, then output. So basically, I just write my truth table into the ROM, then I make use of D-flip flop to record the next state to send to the ROM again which creates a loop.